# Squaring Technique using Vedic Mathematics 

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#### Abstract

Vedic Mathematics provides an interesting approach to modern computing applications by offering an edge of time and space complexities over conventional techniques. Vedic Mathematics consists of sixteen sutras and thirteen sub-sutras, to calculate problems revolving around arithmetic, algebra, geometry, calculus and conics. These sutras are specific to the decimal number system, but this can be easily applied to binary computations. This paper presented an optimised squaring technique using Karatsuba-Ofman Algorithm, and without the use of Duplex property for reduced algorithmic complexity. This work also attempts Taylor Series approximation of basic trigonometric and inverse trigonometric series. The advantage of this proposed power series approximation technique is that it provides a lower absolute mean error difference in comparison to previously existing approximation techniques.


Keywords: COordinate Rotation DIgital Computer (CORDIC), Duplex Property, Maclaurin Series, Squaring, Trigonometric Functions, Taylor Series, Vedic Mathematics.

## I. Introduction

Trigonometric functions are one of the fundamental, yet the most essential core functions in the domain of digital signal processing, digital image processing and high-performance computing applications [1], [2]. This, in turn, demands efficient and optimized hardware implementations. Efficient hardware implementation of trigonometric and inverse trigonometric functions can be employed by using COordinate Rotation DIgital Computer (CORDIC) [3], or Look-Up Table method or power-series method [4]. Among the three methods, CORDIC implementation is the simplest using shift-and-add operations only, but the major disadvantage in it is that it requires a minimum of $n$ iterations which provides high latency and low speed [3]; whereas the look-up table method has a drawback of higher area [4]. But, the power series method provides higher speed system compared to other two methods, but this require an optimized and efficient squaring/multiplication operation and these can be achieved using Vedic Mathematics [5]. Vedic Mathematics consists of sixteen sutras and thirteen sub-sutras and these can be easily applied to binary computations although they are specific to decimal number system [5]. Several researchers have reported Vedic squarers [6]-[12] using the Dwandwa Yoga Duplex property of Urdhwa Tiryagbhyam sutra and Anurupyena sutra of Vedic mathematics [5].

The authors in [4] presented the power series approximation method for calculation of trigonometric functions using the duplex property of binary numbers for squaring operations; but at the cost of algorithm complexity and higher error relative to actual trigonometric functions. This confirms that the use of Duplex property increases algorithm complexity for calculation of trigonometric functions via power series method. In this work, the authors have put an attempt to do power series approximation of trigonometric and inverse trigonometric functions. This paper proposes squaring technique using Vedic Mathematics based on Karatsuba-Ofman Algorithm [4] and without the use of Duplex property for reducing algorithmic complexity.

The rest of the paper is organized as follows: Section II illustrates the power series approximation of trigonometric and inverse trigonometric functions along with its methodology and analysis in Section II-C and Section II-D respectively. Section III explains the proposed squaring technique using Vedic Mathematics. This is followed by conclusion in Section IV.

## II. Power-Series Approximation of Trigonometric and Inverse Trigonometric Functions

## A. Trigonometric Taylor Series

The Taylor series expansion [13] of a real-valued or complex-valued $f(x)$, an infinitely differentiable function at a real or complex point $a$, can be given by

$$
\begin{equation*}
f(x)=\sum_{k=0}^{\infty} \frac{f^{(k)}(a)}{k!}(x-a)^{k} \tag{1}
\end{equation*}
$$

where, $f^{(k)}(a)$ is the $k^{t h}$ derivative of the function $f$ evaluated at the point $a$ (real or complex) and $k$ ! is the factorial of $k$. A Maclaurin series can be considered as a special Taylor series expansion for a particular case when $f(x)$ is evaluated over $a=0$ as follows:

$$
\begin{align*}
f(x)= & \sum_{k=0}^{\infty} \frac{f^{(k)}(0)}{k!}(x)^{k} \\
= & f(0)+f^{\prime}(0) x+\frac{f^{(2)}(0)}{2!} x^{2}+\frac{f^{(3)}(0)}{3!} x^{3}+\ldots  \tag{2}\\
& \quad+\cdots+\frac{f^{(n)}(0)}{k!} x^{k}+\ldots \ldots
\end{align*}
$$

The Maclaurin series expansion of a few important trigonometric functions $(\sin (x), \cos (x), \tan (x))$ and inverse trigonometric functions $\left(\sin ^{-1}(x), \cos ^{-1}(x)\right)$ valid for all real and complex values of $x$ are as follows:

$$
\begin{align*}
\sin (x) & =\sum_{k=0}^{\infty} \frac{(-1)^{k}}{(2 k+1)!} x^{2 k+1}  \tag{3}\\
& =x-\frac{x^{3}}{3!}+\frac{x^{5}}{5!}+\ldots \text { for all values of } x \\
\cos (x) & =\sum_{k=0}^{\infty} \frac{(-1)^{k}}{(2 k)!} x^{2 k}  \tag{4}\\
& =1-\frac{x^{2}}{2!}+\frac{x^{4}}{4!}-\ldots \text { for all values of } x \\
\tan (x) & =\sum_{k=1}^{\infty} \frac{B_{2 n}(-4)^{k}\left(1-4^{k}\right)}{(2 k)!} x^{2 k-1}  \tag{5}\\
& =x-\frac{x^{3}}{3}+\frac{2 x^{5}}{15}-\ldots . . \text { for }|x|<\frac{\pi}{2}
\end{align*}
$$

where $B_{2 n}$ is the Bernoulli series.

$$
\begin{align*}
\sin ^{-1}(x) & =\sum_{k=0}^{\infty} \frac{(2 k)!}{4^{k}(k!)^{2}(2 k+1)} x^{2 k+1}  \tag{6}\\
& =x+\frac{x^{3}}{6}+\frac{3 x^{5}}{40}+\ldots \ldots \text { for }|x|<1
\end{align*}
$$

$$
\begin{align*}
\cos ^{-1}(x) & =\frac{\pi}{2}-\sin ^{-1}(x) \\
& =\frac{\pi}{2}-\sum_{k=0}^{\infty} \frac{(2 k)!}{4^{k}(k!)^{2}(2 k+1)} x^{2 k+1}  \tag{7}\\
& =\frac{\pi}{2}-x-\frac{x^{3}}{6}-\frac{3 x^{5}}{40}-\ldots \text { for }|x|<1
\end{align*}
$$

## B. Basic Mathematical Functions for Binary Approximation

## 1) Ceil Function

This function returns the nearest integer value above the specified number $x$. Symbolically, the ceil of a number $x$ is denoted by $\lceil x\rceil$. Suppose, the fixed-point fractional number is $x=b_{i} \ldots b_{1} b_{0} \cdot b_{-1} b_{-2} \ldots b_{j}$, the corresponding $\lceil x\rceil$ for binary number system can be given as

$$
\begin{align*}
& \left\lceil b_{i} \ldots b_{1} b_{0} \cdot b_{-1} b_{-2} \ldots b_{j}\right\rceil \\
& \quad=b_{i} \ldots b_{1} b_{0}+\left(b_{-1} \oplus b_{-2} \oplus \cdots \oplus b_{j}\right) \tag{8}
\end{align*}
$$

where $\oplus$ is the logical OR operation.

## 2) Floor Function

This function returns the nearest integer value below the specified number $x$. Symbolically, the floor of a number $x$ is denoted by $\lfloor x\rfloor$. Suppose, the fixed-point fractional number is given by $x=b_{i} \ldots b_{1} b_{0} \cdot b_{-1} b_{-2} \ldots b_{j}$, its corresponding $\lfloor x\rfloor$ for binary number system is given as

$$
\begin{equation*}
\left\lfloor b_{i} \ldots b_{1} b_{0} \cdot b_{-1} b_{-2} \ldots b_{j}\right\rfloor=b_{i} \ldots b_{1} b_{0} \tag{9}
\end{equation*}
$$

## 3) Round Function

This function returns the nearest integer value depending on the fractional part of the number. If the fractional part of the number $x$ is equal to or greater than 0.5 , the function returns the smallest integer still above the specified number. Otherwise, the function returns the largest possible integer still smaller than the specified number. Suppose, the fixed-point fractional number is $x=b_{i} \ldots b_{1} b_{0} \cdot b_{-1} b_{-2} \ldots b_{j}$, the round $(x)$ for binary number system is stated as

$$
\begin{equation*}
\operatorname{round}\left(b_{i} \ldots b_{1} b_{0} \cdot b_{-1} b_{-2} \ldots b_{j}\right)=b_{i} \ldots b_{1} b_{0}+b_{-1} \tag{10}
\end{equation*}
$$

## C. Methodology for Power Series Approximation of Trigonometric and Inverse Trigonometric Functions

This subsection elaborates the methodology that can be followed for power series approximation of a few trigonometric and inverse trigonometric series. This is explained in the following.

## 1) Trigonometric Series

a) Sine Series $(\sin (\theta))$

The power series approximation of $\sin (\theta)$ can be given using (3) as

$$
\begin{equation*}
\sin _{a p x}\left(\theta_{a p x}\right)=\theta_{a p x}-\frac{\theta_{a p x}^{3}}{3!\times\left(2^{6}\right)^{3}}+\frac{\theta_{a p x}^{5}}{5!\times\left(2^{6}\right)^{5}} \tag{11}
\end{equation*}
$$

for all the values of $\theta$. Since sine is a periodic function, $\theta$ is valid for $-\pi / 2 \leq \theta \leq \pi / 2$ only.
b) Cosine Series $(\cos (\theta))$

The power series approximation of $\cos (\theta)$ can be given using (4) as

$$
\begin{equation*}
\cos _{a p x}\left(\theta_{a p x}\right)=1-\frac{\theta_{a p x}^{2}}{2!\times\left(2^{6}\right)^{2}}+\frac{\theta_{a p x}^{4}}{4!\times\left(2^{6}\right)^{4}}-\frac{\theta_{a p x}^{6}}{6!\times\left(2^{6}\right)^{6}} \tag{12}
\end{equation*}
$$

for all the values of $\theta$. Since cosine is a periodic function, $\theta$ is valid for $-\pi / 2 \leq \theta \leq \pi / 2$ only.


Fig. 1. Variation of approximated sine series $\left(\sin _{a p x}\left(\theta_{a p x}\right)\right.$ w.r.t sine series $(\sin (\theta))$
c) Tan Series $(\tan (\theta))$

The power series approximation of $\tan (\theta)$ can be given using (5) as

$$
\begin{equation*}
\tan _{a p x}\left(\theta_{a p x}\right)=\theta_{a p x}+\frac{\theta_{a p x}^{3}}{3 \times\left(2^{6}\right)^{3}}+\frac{2 \theta_{a p x}^{5}}{15 \times\left(2^{6}\right)^{5}}+\frac{17 \theta_{a p x}^{7}}{315 \times\left(2^{6}\right)^{7}} \tag{13}
\end{equation*}
$$

for $-\pi / 4<\theta<\pi / 4$.

## 2) Inverse Trigonometric Series

a) Inverse Sine Series $\left(\sin ^{-1}(\theta)\right)$

The power series approximation of $\sin ^{-1}(\theta)$ can be given using (6) as

$$
\begin{align*}
\sin _{a p x}^{-1}\left(\theta_{a p x}\right) & =\frac{\left(\theta_{a p x}\right)}{2^{6}}+\frac{\left(\theta_{a p x}\right)^{3}}{2 \times 3 \times\left(2^{6}\right)^{3}} \\
& +\frac{3 \times\left(\theta_{a p x}\right)^{5}}{2 \times 4 \times 5 \times\left(2^{6}\right)^{5}}+\frac{\left(3 \times 5 \times \theta_{a p x}\right)^{7}}{2 \times 4 \times 6 \times 7 \times\left(2^{6}\right)^{7}} \tag{14}
\end{align*}
$$

for $-0.75 \leq \theta \leq 0.75$.


Fig. 2. Variation of approximated cosine series $\left(\cos _{a p x}\left(\theta_{a p x}\right)\right.$ w.r.t cosine series $(\cos (\theta))$
b) Inverse Cosine Series $\left(\cos ^{-1}(\theta)\right)$

The power series approximation of $\cos ^{-1}(\theta)$ can be giving using (7) as

$$
\begin{align*}
\cos _{a p x}^{-1}\left(\theta_{a p x}\right)= & \frac{\pi}{2}-\sin _{a p x}^{-1}\left(\theta_{a p x}\right) \\
= & \frac{\pi}{2}-\frac{\left(\theta_{a p x}\right)}{2^{6}}-\frac{\left(\theta_{a p x}\right)^{3}}{2 \times 3 \times\left(2^{6}\right)^{3}}  \tag{15}\\
& -\frac{3 \times\left(\theta_{a p x}\right)^{5}}{2 \times 4 \times 5 \times\left(2^{6}\right)^{5}}-\frac{\left(3 \times 5 \times \theta_{a p x}\right)^{7}}{2 \times 4 \times 6 \times 7 \times\left(2^{6}\right)^{7}}
\end{align*}
$$

for $-0.75 \leq \theta \leq 0.75$.
D. Analysis of Approximated Power Series of Trigonometric and Inverse Trigonometric Functions

## 1) Trigonometric Series

a) Sine Series $(\sin (\theta))$

Fig. 1(a) shows the absolute error difference between $\sin _{\text {apx }}\left(\theta_{\text {apx }}\right)$ and $\sin (\theta)$ for different binary approximations explained in Section II-B. The plot for sine approximations $\left(\sin (\theta)\right.$ and $\left.\sin _{\text {apx }}\left(\theta_{a p x}\right)\right)$ with the best binary approximation for $\theta_{\text {apx }}$ is also shown in Fig. 1(b). From Fig. 1(b), it can be shown that the maximum possible error is $7.32 \times 10^{-3}$ and the mean absolute error difference is $4.6 \times 10^{-3}$.


Fig. 3. Variation of approximated $\tan$ series $\left(\tan _{a p x}\left(\theta_{a p x}\right)\right.$ w.r.t $\tan$ series $(\tan (\theta))$
b) Cosine Series $(\cos (\theta))$

Fig. 2(a) shows the absolute error difference between $\cos _{a p x}\left(\theta_{a p x}\right)$ and $\cos (\theta)$ for different binary approximations explained in Section II-B. The plot for cosine approximations $\left(\cos (\theta)\right.$ and $\left.\cos _{a p x}\left(\theta_{a p x}\right)\right)$ with the best binary approximation for $\theta_{a p x}$ is also shown in Fig. 2(b). The maximum possible error is $8.25 \times 10^{-3}$ and the mean absolute error difference is $1.23 \times 10^{-4}$ clearly shown in Fig. 2(b).
c) Tan Series $(\tan (\theta))$

Fig. 3(a) shows the absolute error difference between $\tan _{a p x}\left(\theta_{a p x}\right)$ and $\tan (\theta)$ for different binary approximations explained in Section II-B. The plot for $\tan$ approximations $\left(\tan (\theta)\right.$ and $\left.\tan _{\text {apx }}\left(\theta_{a p x}\right)\right)$ with the best binary approximation for $\theta_{a p x}$ is also shown in Fig. 3(b). From Fig. 3(b), it can be shown that the maximum possible error is $11 \times 10^{-3}$ and the mean absolute error difference is $9.7 \times 10^{-3}$.

## 2) Inverse Trigonometric Series

a) Inverse Sine Series $\left(\sin ^{-1}(\theta)\right)$

Fig. 4(a) shows the absolute error difference between $\sin _{\text {apx }}^{-1}\left(\theta_{a p x}\right)$ and $\sin ^{-1}(\theta)$ for different binary approximations explained in Section II-B. The plot for inverse sine approximations $\left(\sin ^{-1}(\theta)\right.$ and $\left.\sin _{a p x}^{-1}\left(\theta_{a p x}\right)\right)$ with the


Fig. 4. Variation of approximated inverse sine series $\left(\sin _{a p x}^{-1}\left(\theta_{a p x}\right)\right.$ w.r.t sine series $\left(\sin ^{-1}(\theta)\right)$
best binary approximation for $\theta_{a p x}$ is also shown in Fig. 4(b). The maximum possible error is $4.2 \times 10^{-3}$ and the mean of absolute error difference is $7.5 \times 10^{-3}$ clearly shown in Fig. 4(b).
b) Inverse Cosine Series $\left(\cos ^{-1}(\theta)\right)$

Fig. 5(a) shows the absolute error difference between $\cos _{a p x}^{-1}\left(\theta_{a p x}\right)$ and $\cos ^{-1}(\theta)$ for different binary approximations explained in Section II-B. The plot for Inverse cos approximations $\left(\cos ^{-1}(\theta)\right.$ and $\left.\cos _{a p x}^{-1}\left(\theta_{a p x}\right)\right)$ with the best binary approximation for $\theta_{a p x}$ is also shown in Fig. 5(b). From Fig. 5(b), it can be shown that the maximum possible error is $4.2 \times 10^{-3}$ and the mean absolute error difference is $7.5 \times 10^{-3}$.

Table I gives the comparison of absolute mean error difference of the proposed work with existing power series approximations of trigonometric series [4]. $\sin (\theta)$ and $\cos (\theta)$ gives better results than [4]; although $\tan (\theta)$ does not give better performance due to a high relative error near $\theta=-\frac{\pi}{4}$.

The absolute mean difference of the inverse trigonometric series is also summarized in Table II in reference to Fig. 3 and Fig. 4. From Table II it is observed that $\sin ^{-1}(\theta)$ and $\cos ^{-1}(\theta)$ produces the same absolute error difference of $7.5 \times 10^{-3}$. This is due to the fact that $\cos ^{-1}(\theta)=\frac{\pi}{2}-\cos ^{-1}(\theta)$ and the same error is reflected in both the sine and cosine inverse series.


Fig. 5. Variation of approximated inverse cosine series $\left(\cos _{a p x}^{-1}\left(\theta_{a p x}\right)\right.$ w.r.t cosine series $\left(\cos ^{-1}(\theta)\right)$

TABLE I
Comparison of the Proposed Work with Existing Power Series Approximations of Trigonometric Series

| Trigonometric Series | Absolute Error Difference |  |
| :---: | :---: | :---: |
|  | Tiwari [4] | Proposed |
| $\sin (\theta)$ | $6.3 \times 10^{-3}$ | $4.6 \times 10^{-3}$ |
| $\cos (\theta)$ | $3.6 \times 10^{-3}$ | $1.23 \times 10^{-4}$ |
| $\tan (\theta)$ | $4.6 \times 10^{-3}$ | $9.7 \times 10^{-3}$ |

TABLE II
Absolute Error Difference Values of Inverse Trigonometric Series Approximations

| Inverse Trigonometric Series | Absolute Error Difference |
| :---: | :---: |
| $\sin ^{-1}(\theta)$ | $7.5 \times 10^{-3}$ |
| $\cos ^{-1}(\theta)$ | $7.5 \times 10^{-3}$ |

## III. Proposed Vedic Squaring Technique

Let us illustrate the proposed squaring technique taking an example for calculating the square of a binary number $A$ with $n$-bit length. $A$ can be partitioned into two equal-sized binary numbers: $A_{H}$ and $A_{L}$, using Karatsuba-Ofman algorithm (K-O) algorithm [14]. Note: The subscripts $H$ and $L$ represent higher-order $n / 2$ bits and lower-order $n / 2$ bits respectively only if $n$ is even. If $n$ is odd, the MSB bit of $A_{H}$ need to be padded with zeros so that $A_{H}$ and $A_{L}$ are of equal $n / 2$ bit-length [4].

The square of $A$ can be calculated in terms of $A_{H}$ and $A_{L}$ and stated as follows:

$$
\begin{equation*}
A^{2}=A \times A=2^{n}\left(A_{H}^{2}\right)+2^{\frac{n}{2}+1}\left(A_{H} \times A_{L}\right)+\left(A_{L}^{2}\right) \tag{16}
\end{equation*}
$$

$A_{H} \times A_{L}$ can be easily calculated with the help of Urdhwa Tribhagyam Sutra of Vedic multiplication for binary numbers [5]. The terms $A_{H}^{2}$ and $A_{L}^{2}$ in 16 can be further split up recursively to two-bit binary numbers for squaring up using K-O algorithm. Next, the conditional squarer can be realized for squaring two-bit binary number with the following cases:

$$
\begin{align*}
& \text { If } A=00, A^{2}=0000 \\
& \text { If } A=11, A^{2}=1001 \\
& \text { If } A=10, A^{2}=0100  \tag{17}\\
& \text { If } A=01, A^{2}=0001
\end{align*}
$$

For better understanding of the proposed squaring technique using 16 , let us take another example $A=1101$. Here, $n=4$, therefore, the two equal-sized binary numbers are: $\left(A_{H}\right)=11$ and $\left(A_{L}\right)=01$. Using (17) of the conditional squarer, the values of $A_{H}^{2}$ and $A_{L}^{2}$ are $=1001$ and 0001 respectively. Next, the value of $A_{H} \times A_{L}=0011$ can be easily calculated using Urdhwa Tribhagyam Sutra algorithm. Since $n=4$, the left shifting of obtained $A_{H}^{2}$ by $n$ bits result into 10010000 ; similarly, the left-shift of $A_{H} \times A_{L}$ by $\left(\frac{n}{2}+1\right)$ bits give 0011000 . Lastly, adding $A_{L}^{2}$ and the left-shifted values of $A_{H} \times A_{L}, A_{H}^{2}$ by $\frac{n}{2}+1$ bits and $n$, respectively gives the value of $A^{2}=010101001$. This explains the squaring operation of $A$ using (16). The major advantage of the proposed squaring technique is square calculation of a binary number without the use of the "Duplex" property of binary numbers providing lesser algorithmic complexity [4], [5], .

Future works focuses on the hardware implementation of the power series approximation of trigonometric and inverse trigonometric functions. This aims towards high-speed architecture for integers. Preliminary methodology for conversion of floating-point numbers to approximated binary numbers are already analyzed in detailed in Section II. A close observation of subsection states that, for hardware implementation of approximated power series (for trigonometric and inverse trignometric functions) requires squarer, multiplier, divider and factorial circuitry [1], [15], [16]. Hardware implementation of these circuits is beyond the scope of this paper; but these will be considered as future works.

## IV. Conclusions

This paper presents an efficient Vedic squaring technique without the use of Duplex property and it can easily be extended for any $n$-bit binary numbers. The main advantage of this proposed technique is that it focuses on reduced algorithmic complexity. This work also presented the analytical study of Taylor series approximation of trigonometric functions: $\sin (x), \cos (x), \tan (x))$ and inverse trigonometric functions: $\sin ^{-1}(x), \cos ^{-1}(x)$. Future works will focus on hardware implementations of the proposed power series approximations on digital VLSI architectures using FPGAs and ASICs. Experimental results might reflect great potential in digital arithmetic for prospective exploration of high-performance and embedded computing systems.

## REFERENCES

[1] J. Pihl and E. J. Aas, "A multiplier and squarer generator for high performance DSP applications," in Proceedings of the 39th Midwest Symp. on Circuits and Systems, vol. 1, 1996, pp. 109-112.
[2] A. Kant and S. Sharma, "Applications of Vedic multiplier designs - A review," in 2015 4th Int. Conf. on Reliability, Infocom Technologies and Optimization (ICRITO) (Trends and Future Directions), 2015, pp. 1-6.
[3] K. Parhi, VLSI Digital Signal Processing Systems: Design and Implementation. Wiley India Pvt. Limited, 2007.
[4] H. D. Tiwari, "Vedic processor based trigonometric calculations using power series," in 2019 5th Int. Conf. on Advanced Computing and Communication Systems (ICACCS), 2019, pp. 118-123.
[5] Jagadguru Swami Sri Bharati Krishna Tirthaji Maharaja, Vedic Mathematics: Sixteen Simple Mathematical Formulae from the Vedas. Motilal Banarsidass Publishers, 012005.
[6] L. Sriraman, K. Kumar, and T. N. Prabakar, "Design and FPGA implementation of binary squarer using Vedic mathematics," in 2013 4th Int. Conf. on Computing, Communications and Networking Technologies (ICCCNT). Los Alamitos, CA, USA: IEEE Computer Society, July 2013, pp. 1-5.
[7] S. Barve, S. Raveendran, C. Korde, T. Panigrahi, Y. B. Nithin Kumar, and M. H. Vasantha, "FPGA Implementation of Square and Cube Architecture Using Vedic Mathematics," in 2018 IEEE Int. Symp. on Smart Electronic Systems (iSES) (Formerly iNiS), 2018, pp. 6-10.
[8] H. Thapliyal, S. Kotiyal, and M. B. Srinivas, "Design and Analysis of a novel parallel square and cube architecture based on ancient Indian Vedic mathematics," in 48th Midwest Symp. on Circuits and Systems, 2005., vol. 2, 2005, pp. 1462-1465.
[9] S. Jalaja and A. M. V. Prakash, "High Speed VLSI Architecture for Squaring Algorithm Using Retiming Approach," in 3rd Int. Conf. on Advances in Computing and Communications, 2013, pp. 233-238.
[10] S. Akhter and S. Chaturvedi, "Modified Binary Multiplier Circuit Based on Vedic Mathematics," in 6th Int. Conf. on Signal Processing and Integrated Networks (SPIN), 2019, pp. 234-237.
[11] A. Kumar, D. Kumar, and Siddhi, "Hardware Implementation of $16 \times 16$ bit Multiplier and Square using Vedic Mathematics," 2012.
[12] S. Patil, D. V. Manjunatha, and D. Kiran, "Design of speed and power efficient multipliers using Vedic mathematics with VLSI implementation," in Int. Conf. on Advances in Electronics Computers and Communications, 2014, pp. 1-6.
[13] A. T. Claudio Canuto, Mathematical Analysis I, 2nd ed. Springer International Publishing, 2015.
[14] H. Thapliyal and M. Srinivas, "High Speed Efficient N $\times$ N Bit Parallel Hierarchical Overlay Multiplier Architecture Based On Ancient Indian Vedic Mathematics," 122004.
[15] P. Saha, A. Banerjee, A. Dandapat, and P. Bhattacharyya, "ASIC design of a high speed low power circuit for factorial calculation using Ancient Vedic mathematics," Microelectron. J., vol. 42, pp. 1343-1352, 2011.
[16] P. Saha, A. Banerjee, P. Bhattacharyya, and A. Dandapat, "Vedic Divider: Novel Architecture (ASIC) for High Speed VLSI Applications," in 2011 Int. Symp. on Electronic System Design, 2011, pp. 67-71.

